

FIG. 1

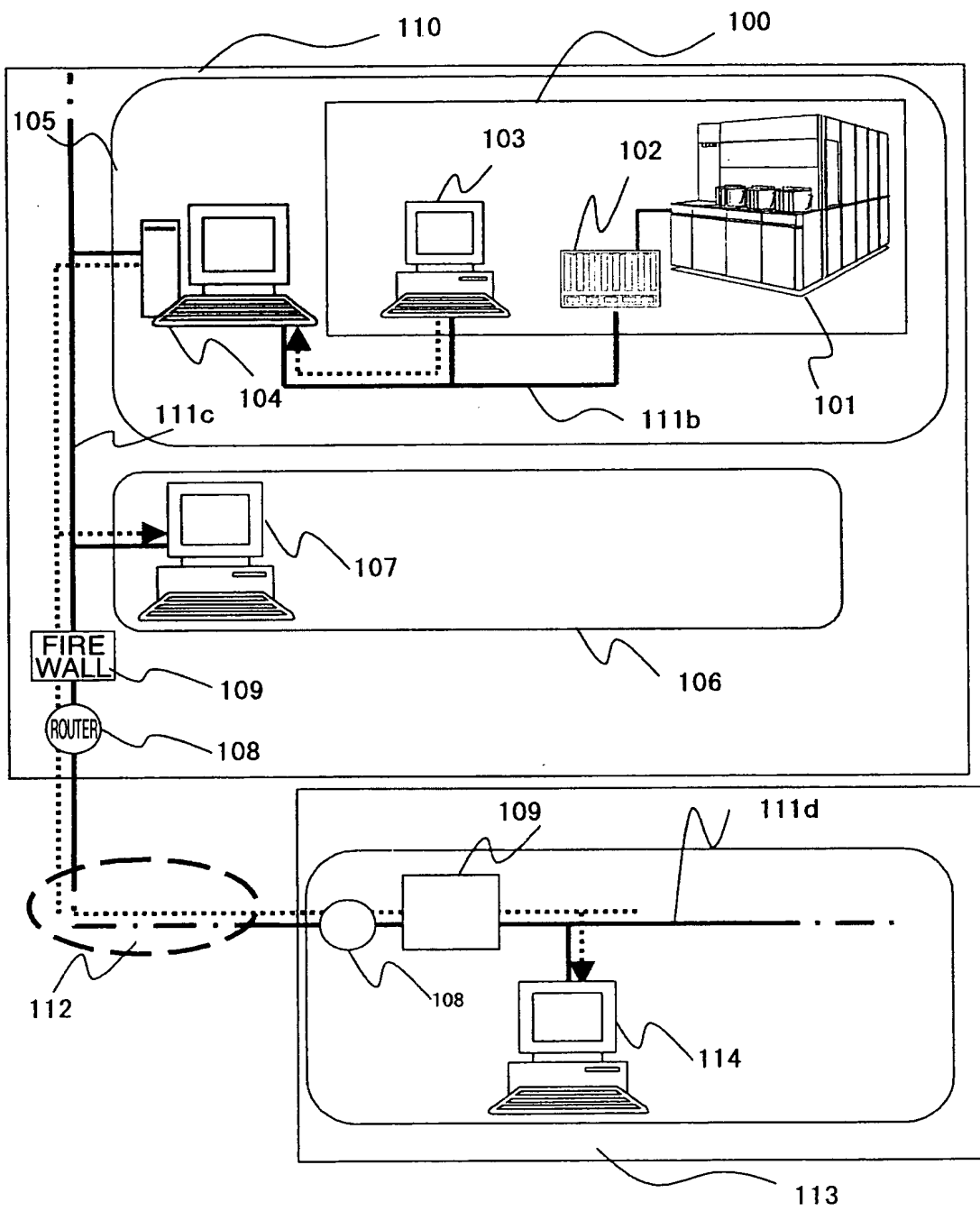


FIG. 2

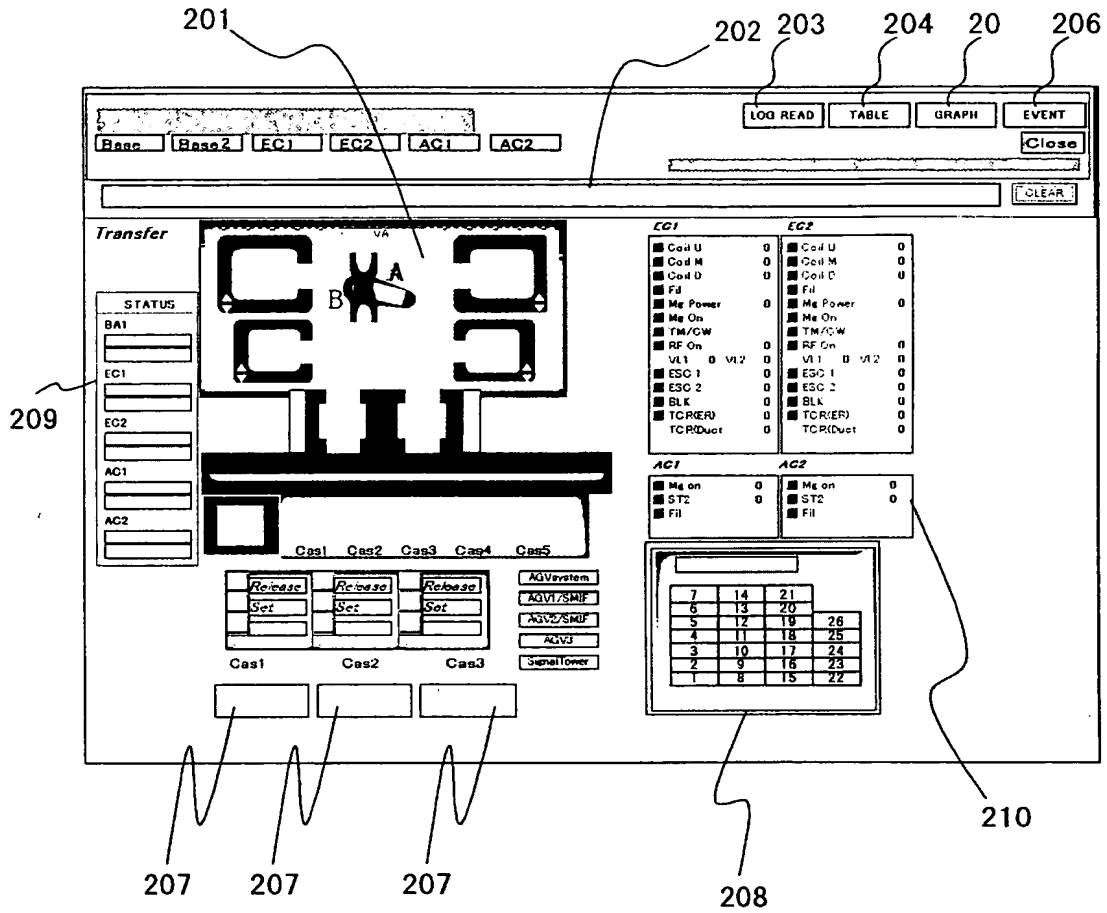
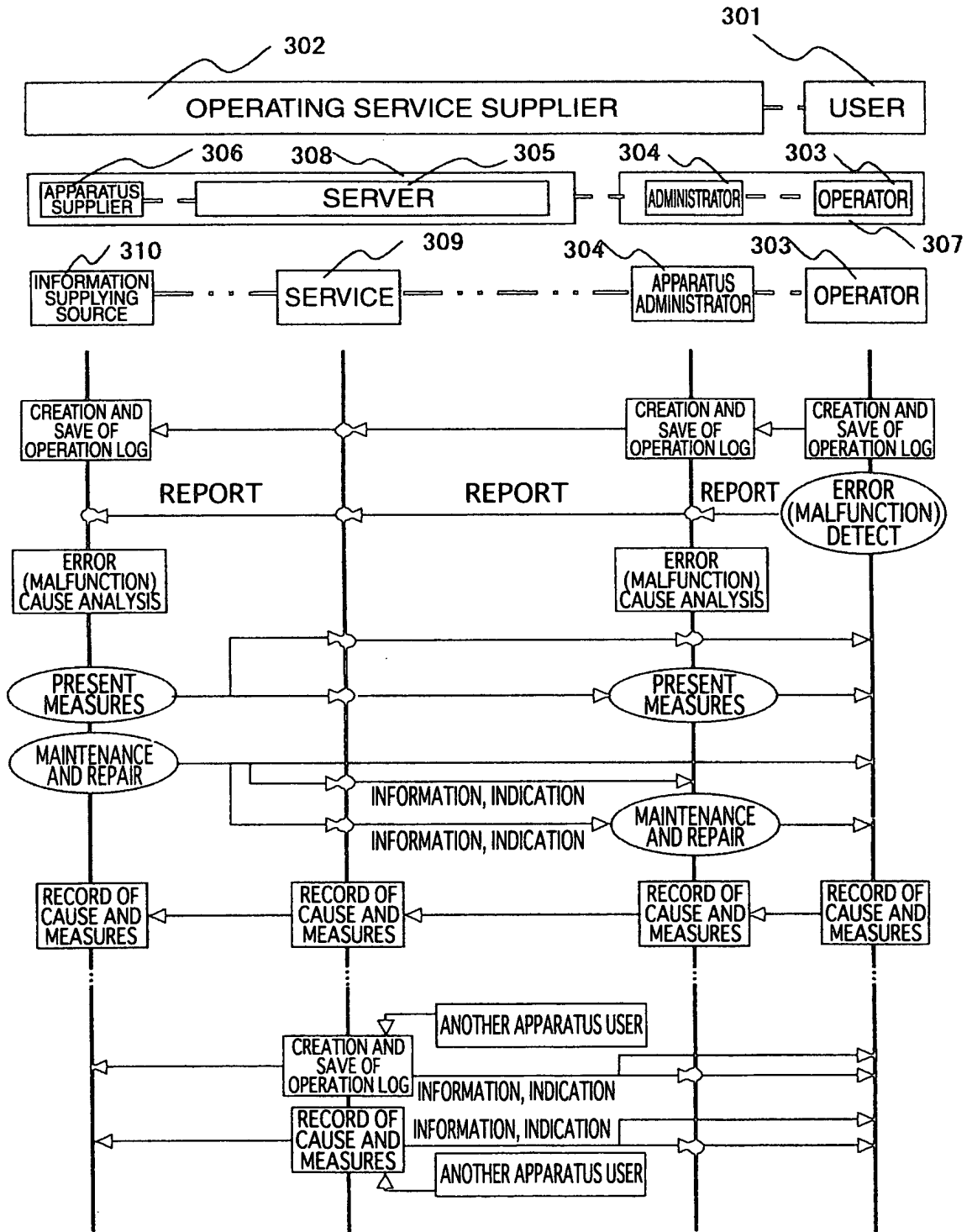


FIG. 3



202204523001

FIG. 4A

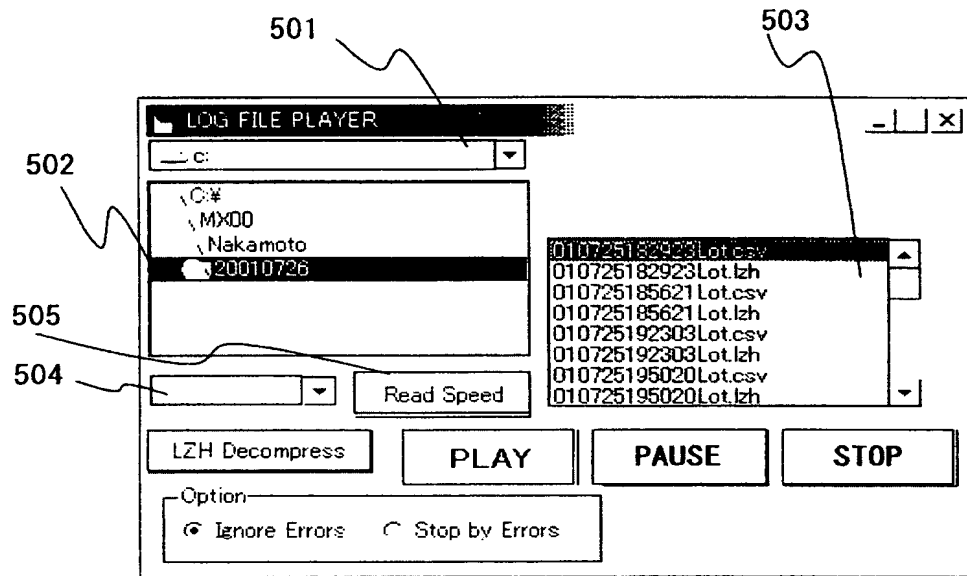
						LOG READ	TABLE	GRAPH	EVENT	
Base	Base2	EG1	EG2	AC1	AC2	Close				
01/10/29 16:42:03 ERROR(BA1)12 LL chamber exhaust time out error										CLEAR

FIG. 4B

						LOG READ	TABLE	GRAPH	EVENT	
Base	Base2	EG1	EG2	AC1	AC2	Close				
Details: set pressure not detected passing predetermined time after exhaust LL chamber (VCSB:100Pa/0.15Torr)										OK

20220719 15:28:00

FIG. 5



202207140001

FIG. 6

Event Window

Time	AREA	EVENT NAME
01/07/25 18:28:23	ET1	AUTOMATIC DRIVE START
01/07/25 18:29:23	ET2	AUTOMATIC DRIVE START
01/07/25 18:29:23	AT1	AUTOMATIC DRIVE START
01/07/25 18:29:23	BA1	AUTOMATIC DRIVE STOP SW EFFECTIVE
01/07/25 18:29:24	BA1	AUTOMATIC DRIVE STOP SW DISABLED
01/07/25 18:30:58	BA1	CASSETTE 2 TEMPORARY STOP SW EFFECTIVE
01/07/25 18:30:58	BA1	CASSETTE 2 SUPPLY STOP SW EFFECTIVE
01/07/25 18:31:01	BA1	CASSETTE 2 EACH WAFER PRODUCT PROCESS START
01/07/25 18:32:03	ET2	ETCHING START
01/07/25 18:32:25	ET2	START DISCHARGE (SKIP SW IN EFFECT)

Select Area

☒ ALL
☐ BA1
☐ EC1
☐ EC2
☐ AC1
☐ AC2

Select

CLEAR

Time	Wafer No	FROM	TO
01/07/25 18:31:07	2 - 1	Port2	----> ATM. Robot
01/07/25 18:31:18	2 - 1	ATM. Robot	----> Alignment Unit
01/07/25 18:31:34	2 - 1	Alignment Unit	----> ATM. Robot
01/07/25 18:31:42	2 - 1	ATM. Robot	----> Load Lock
01/07/25 18:31:51	2 - 2	Port2	----> ATM. Robot
01/07/25 18:31:58	2 - 1	Load Lock	----> VAC. Robot-A
01/07/25 18:32:02	2 - 2	ATM. Robot	----> Alignment Unit
01/07/25 18:32:06	2 - 1	VAC. Robot-A	----> Etching2
01/07/25 18:32:16	2 - 1	EC2: Start Process	
01/07/25 18:32:18	2 - 2	Alignment Unit	----> ATM. Robot
01/07/25 18:32:26	2 - 2	ATM. Robot	----> Load Lock
01/07/25 18:32:35	2 - 3	Port2	----> ATM. Robot
01/07/25 18:32:42	2 - 2	Load Lock	----> VAC. Robot-A
01/07/25 18:32:45	2 - 3	ATM. Robot	----> Alignment Unit

CLEAR

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FIG. 7

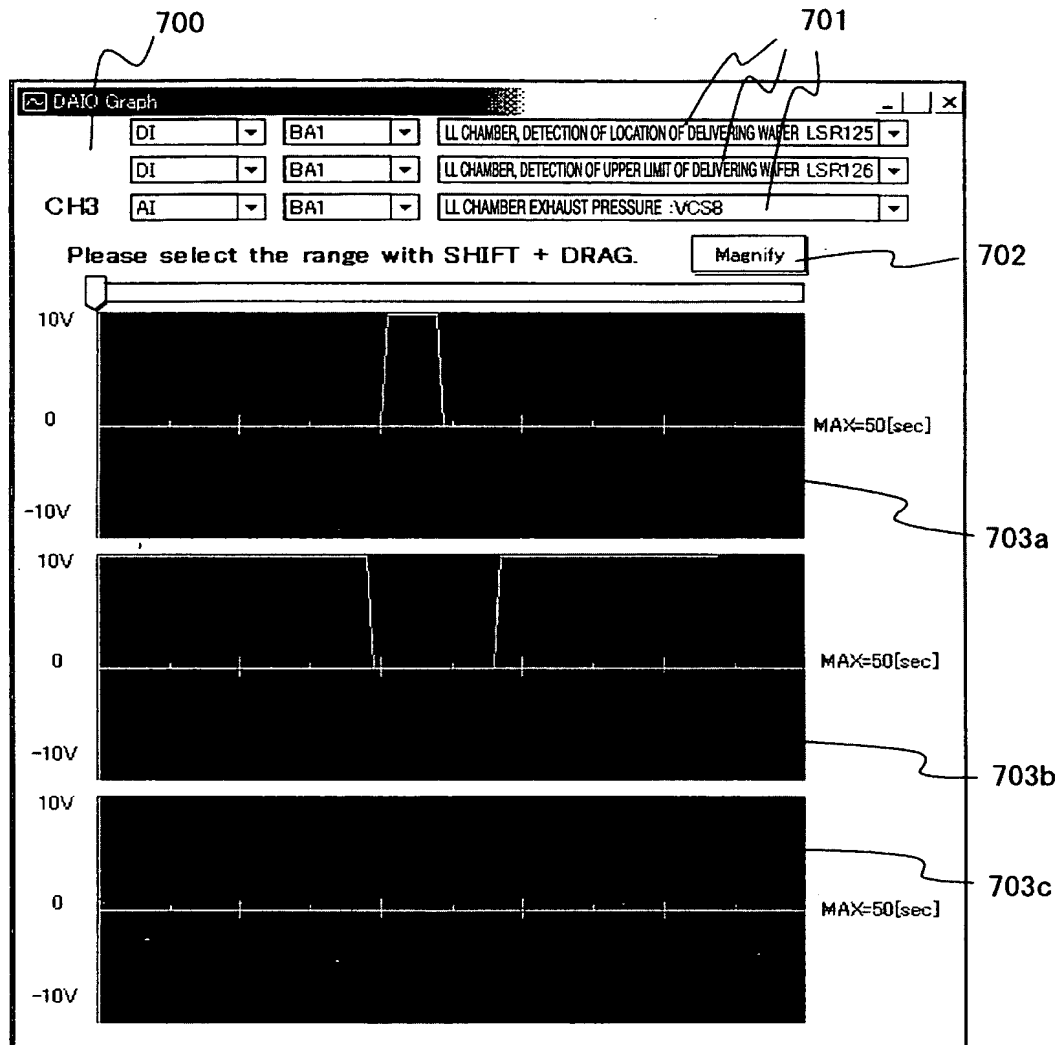


FIG. 8

ID TABLE							
ID	Attr	Byte	bit	Value	Name	Condition	
31	DO	3	6	0	LP1 READY		▲
34	DO	4	1	0	LP1 HO-AVBL SMIF1 Ready to UL		
32	DO	3	7	0	LP1 ES		
35	DO	4	2	0	LP1 LREQ SMIF1 Port Lock		
33	DO	4	0	0	LP1 UREQ SMIF1 Load/Unload		▼
ID	Attr	Byte	bit	Value	Name	Condition	
415	DI	5	6	0	LP1 TR REQ		▲
414	DI	5	5	0	LP1 CS2		
409	DI	5	0	0	LP1 BUSY		
416	DI	5	7	0	LP1 CONT		
410	DI	5	1	0	LP1 COMPT		
413	DI	5	4	0	LP1 CS1		
412	DI	5	3	0	LP1 CS0		
411	DI	5	2	0	LP1 VALID		▼

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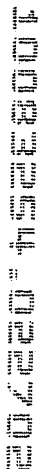


FIG. 10

LIST OF OCCURRENCE ERROR CAUSES	
ERROR: TIMEOUT ERROR OF DELIVERING LOCATION OF LL CHAMBER	
LARGER POSSIBILITY ↑	DELIVERING LOCATION SENSOR OF LL CHAMBER
	LOWER LIMIT LOCATION SENSOR OF LL CHAMBER
	UPPER LIMIT LOCATION SENSOR OF LL CHAMBER
	VACUUM DETECTION SENSOR OF LL CHAMBER
	VACUUM GAUGE OF LL CHAMBER